

## Areas of Interest

1. Secure Components and Technologies
2. Enhanced Security for High Performance Components
3. Access Denial Solutions
4. Advanced Packaging Architectures
5. Microelectronics Sensors
6. Enhanced Secure Boot
7. Encryption
8. Environmentally Stable PUFs
9. AT Penalties
10. Modelling, Simulation, and Emulation Tools and Capabilities
11. AT Countermeasures and Architectures
12. ATEA Assessment Capabilities
13. Other