



2017 Anti-Tamper Conference



11-13 April 2017

Johns Hopkins University/Applied Physics Laboratory

Kossiakoff Center - Laurel, MD 20723

**“Anti-Tamper Implementation: The Good, The Bad, and The Ugly of Systems Engineering”
Tuesday 11 April 2017**

Welcome

0800-0805 SPACER.....

Session 1: Government Updates and Perspective

0810-0855 Anti-Tamper Executive Agent Update

0855-0905 Army Anti-Tamper Perspective.....

0905-0915 Department of Navy Anti-Tamper Perspective.....

0915-0925 Air Force Anti-Tamper Perspective.....

0925-0935 MDA Anti-Tamper Perspective.....

0935-0945 Anti-Tamper Program Office Update.....

0945-1040 Technical Implementation Guide.....

1040-1100 Break

Keynote Address

1100-1200.....

1200-1315 Lunch

1315-1325 Review 2017 AT Conference Theme.....

Session 2: The Good, The Bad, and The Ugly

1325-1330 Session Review.....

1330-1400 Requirements and Process Structure for Effective Field Returns.....

1400-1430 Maintaining Deployed Systems with AT.....

1430-1500 Case Study: Flexible Architecture Responds to Evolving Threat.....

1500-1520 Break

1520-1540 FPGA Assessments Overview.....

1540-1610 Anti-Tamper Assessments.....

1610-1640 Anti-Tamper Evaluation Team Working Group.....

1640-1510 Air Force Anti-Tamper Evaluation Team Lessons Learned.....

1710-1730 Adjourn

**“Anti-Tamper Implementation: The Good, The Bad, and The Ugly of Systems Engineering”
Wednesday 12 April 2017**

Welcome

0800-0805 SPACER.....

Session 3: New Attacks and Countermeasures

0805-0810 Session Review

0810-0840 Technology Development.....

0840-0910 Characterization Results: PUF on the Xilinx Zynq UltraScale + MPSoC

0910-0940 Additive Manufacturing for Volume Protection of Systems in Package

0940-1010 Electromagnetic Fault Induction on Field-Programmable Gate Arrays.....

1010-1040 Non-Volatile Philosophy of Memory.....

1040-1100 Break

Keynote Address

1100-1200.....

1200-1315 Lunch

Session 4: New Tools and Methods

1315-1320 Session Review.....

1320-1350 The Open Mission Systems AT Interface Control Document

1350-1420 Irrefutable Tamper Logging through FPGA Key Management

1420-1450 Application of TVLA to Validate a Commercial Public Key Cryptography.....

1450-1510 Break

1510-1540 Tamper Detection System without Special Hardware

1540-1640 Trusted Foundry Keynote.....

1640-1700 Adjourn

Networking Event at John Hopkins Applied Physics Lab

1700-1830.....

**“Anti-Tamper Implementation: The Good, The Bad, and The Ugly of Systems Engineering”
Thursday 13 April 2017**

Welcome

0800-0805 SPACER.....

Session 5: Navigating the AT and Cyber Boundary

0805-0810 Session Review

0810-0840 Embedded Cyber & AT

0840-0910 An Integrated Design Supporting AT and Cyber Requirements.....

0910-0940 The Marriage of IA/Cyber and Hardening in Secure Processing.....

0940-1040 AT and Cyber Service Lead Panel / Q&A

1040-1100 Break

Keynote Address

1100-1200.....

1200-1315 Lunch

Session 6: AT Threat Update

1315-1320 Session Review.....

1320-1350 AT Intel Briefing.....

1350-1420 NASIC

1420-1450 AT Threat Report.....

1450-1520 Closing Comments

1520-1600 Adjourn